Docket No: 241433US2S

IN THE UNITED STATES I

# IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

Yoshiaki ASAO : EXAMINER:

SERIAL NO: 10/637,662

FILED: August 11, 2003 : GROUP ART UNIT: 1732

FOR: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

FILING OF SUPPLEMENTAL APPLICATION DATA SHEET

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) submit herewith a Supplemental Application Data Sheet for the purpose of adding a residence address and providing complete information for the inventor.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

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22850

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### APPLICATION INFORMATION

Application Number::

10/637,662

**Application Date::** 

08/11/03

Application Type::

REGULAR

Subject Matter::

UTILITY

CD-ROM or CD-R?::

NONE

Title::

SEMICONDUCTOR INTEGRATED

CIRCUIT DEVICE

**Attorney Docket Number::** 

241433US2S

**Total Drawing Sheets::** 

16

#### INVENTOR INFORMATION

Applicant Authority Type::

**INVENTOR** 

Primary Citizenship Country::

Japan

Status::

**FULL CAPACITY** 

Given Name::

Yoshiaki

Family Name::

**ASAO** 

City of Residence::

Sagamihara-shi

Country of Residence::

Japan

Street of Mailing Address::

c/o Intellectual Property Division, Toshiba

Corporation, 1-1, Shibaura 1-chome

City of Mailing Address::

Minato-ku

State or Province of Mailing Address::

Tokyo

Country of Mailing Address::

Japan

Postal or Zip Code of Mailing Address::

105-8001

### CORRESPONDENCE INFORMATION

Correspondence Customer Number::

22850

# REPRESENTATIVE INFORMATION

Representative Customer Number::

22850

#### FOREIGN PRIORITY INFORMATION

Application Number:	Country::	Filing Date::	Priority Claimed::
2002-235070	Japan	08/12/02	YES